



CV116	Stepper Mode Enable	29-32	25-28	21-24	17-20	13-16	9-12	5-8	1-4	Line bit set = enable
CV117	Stepper Mode Enable	61-64	57-60	53-56	49-52	45-48	41-44	37-40	33-36	Line bit set = enable
118-120	3 Unused									
8 Unused										
129-640	Manufacturer Unique 512 bytes									
Input/Output Control = 512 bytes										
<p>Input/Output Control bits:  aaaaaaa = Low Address,  t s mm hhhh  t = paired  s = Signal (output only)  mm = mode  00 - Switch Request  01 - Switch Feedback  10 - Sensor Message  11 - Reserved  hhhh = Hi Address, Factory Default = 0  d <b>D</b>- s2s2 - s1s1 - tt  d = Output Direction normal/inverted  D = In/Out info for each line  ss = Secondary transition control  00 - No Second Message  01 - on Positive Edge  10 - on Negative Edge  11 - on Both Transitions  tt = Primary transition control  00 - No Primary Message  01 - on Positive Edge  10 - on Negative Edge  11 - on Both Transitions  i - ttt - TTTT  i = Trigger 0 - normal 1 - inverted  ttt = Output type  000 - Short Pulse / Input debounce time  001 - Long Pulse  010 - Short Blink / Input toggle 'on' time  011 - Long Blink  <b>100 - Reserved</b>  101 - Reserved  110 - Reserved  111 - Reserved  TTTT = Timing Same set as TC-64  Sec #1 and #2  aaaaaaa = lo address bits  d - p - tt - hhhh  d Inverted</p>										

10 - Respond Normal 11 - Respond Inverted hhhh=hid=direction p=polarity tt=type, 00 - Send Normal 01 - Sen add										
I/O #1 I/O control words										
CV129	aaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV130	t=alt/precision, s=Sig, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV131	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV132	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV133	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV134	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV135	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV136	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #2 I/O control words										
CV137	aaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV138	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV139	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV140	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV141	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV142	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV143	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV144	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #3 I/O control words										
CV145	aaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV146	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV147	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV148	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV149	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV150	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV151	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV152	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"

I/O #4 I/O control words										
CV153	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV154	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV155	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV156	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV157	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV158	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV159	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV160	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #5 I/O control words										
CV161	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV162	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV163	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV164	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV165	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV166	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV167	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV168	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #6 I/O control words										
CV169	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV170	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV171	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV172	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV173	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV174	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV175	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV176	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #7 I/O control words										
CV177	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV178	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV179	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV180	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control

CV181	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV182	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV183	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV184	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #8 I/O control words										
CV185	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV186	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV187	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV188	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV189	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV190	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV191	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV192	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #9 I/O control words										
CV193	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV194	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV195	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV196	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV197	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV198	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV199	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV200	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #10 I/O control words										
CV201	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV202	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV203	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV204	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV205	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV206	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV207	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV208	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #11 I/O control words										



CV238	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV239	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV240	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #15 I/O control words										
CV241	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV242	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV243	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV244	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV245	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV246	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV247	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV248	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
I/O #16 I/O control words										
CV249	aaaaaaaa=lo addr, hhh=hi addr	a	a	a	a	a	a	a	a	Low address
CV250	t=alt/precision, mm=mode	t	s	m	m	h	h	h	h	control / High address
CV251	d=dir, D=port I/O, ss=sec trans, tt=transition	d	D	s <sup>2</sup>	s <sup>2</sup>	s <sup>1</sup>	s <sup>1</sup>	t	t	Dir and Transition
CV252	i=inv, ttt=type, TTTT=Timing	i	t	t	t	T	T	T	T	I/O control
CV253	Sec #1 lo address bits	a	a	a	a	a	a	a	a	Secondary #1
CV254	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV255	Sec #2 lo address bits	a	a	a	a	a	a	a	a	Secondary #2
CV256	d=dir, p=pol, tt=type, hhhh=hi addr	d	p	t	t	h	h	h	h	"
CV257-384 I/O #17-32										
CV385-512 I/O #33-48										
CV513-640 I/O #49-64										
CV641-896 = 256 bytes used for Stepper Table <b>NOT Implemented!</b>										
Notes:										
Mode/Operation:										
Stepper Table entry #1										
CV641	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type
CV642	Position 1 ls	l	l	l	l	l	l	l	l	
CV643	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV644	Option type 1 / speed 1	t	t	t	s	s	s	s	s	







CV711	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	100 = repeat left 101 = repeat right 110 = xx 111 = stop (sets position = 0)  sssss = Speed
CV712	Option type 2 / speed 2	t	t	t	s	s	s	s	s	
CV713	Position 3 ms	m	m	m	m	m	m	m	m	
CV714	Position 3 ls	l	l	l	l	l	l	l	l	
CV715	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	
CV716	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV717	Position 4 ms	m	m	m	m	m	m	m	m	
CV718	Position 4 ls	l	l	l	l	l	l	l	l	
CV719	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV720	Option type 4 / speed 4	t	t	t	s	s	s	s	s	
<b>Stepper Table entry #6</b>										
CV721	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type 000 = normal 001 = rebound 010 = continuous left 011 = continuous right 100 = repeat left 101 = repeat right 110 = xx 111 = stop (sets position = 0)  sssss = Speed
CV722	Position 1 ls	l	l	l	l	l	l	l	l	
CV723	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV724	Option type 1 / speed 1	t	t	t	s	s	s	s	s	
CV725	Position 2 ms	m	m	m	m	m	m	m	m	
CV726	Position 2 ls	l	l	l	l	l	l	l	l	
CV727	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	
CV728	Option type 2 / speed 2	t	t	t	s	s	s	s	s	
CV729	Position 3 ms	m	m	m	m	m	m	m	m	
CV730	Position 3 ls	l	l	l	l	l	l	l	l	
CV731	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	
CV732	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV733	Position 4 ms	m	m	m	m	m	m	m	m	
CV734	Position 4 ls	l	l	l	l	l	l	l	l	
CV735	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV736	Option type 4 / speed 4	t	t	t	s	s	s	s	s	
<b>Stepper Table entry #7</b>										
CV737	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type 000 = normal 001 = rebound 010 = continuous left 011 = continuous right 100 = repeat left 101 = repeat right
CV738	Position 1 ls	l	l	l	l	l	l	l	l	
CV739	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV740	Option type 1 / speed 1	t	t	t	s	s	s	s	s	
CV741	Position 2 ms	m	m	m	m	m	m	m	m	
CV742	Position 2 ls	l	l	l	l	l	l	l	l	
CV743	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	

CV744	Option type 2 / speed 2	t	t	t	s	s	s	s	s	110 = xx 111 = stop (sets position = 0)  sssss = Speed
CV745	Position 3 ms	m	m	m	m	m	m	m	m	
CV746	Position 3 ls	l	l	l	l	l	l	l	l	
CV747	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	
CV748	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV749	Position 4 ms	m	m	m	m	m	m	m	m	
CV750	Position 4 ls	l	l	l	l	l	l	l	l	
CV751	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV752	Option type 4 / speed 4	t	t	t	s	s	s	s	s	
<b>Stepper Table entry #8</b>										
CV753	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type 000 = normal 001 = rebound 010 = continuous left 011 = continuous right 100 = repeat left 101 = repeat right 110 = xx 111 = stop (sets position = 0)  sssss = Speed
CV754	Position 1 ls	l	l	l	l	l	l	l	l	
CV755	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV756	Option type 1 / speed 1	t	t	t	s	s	s	s	s	
CV757	Position 2 ms	m	m	m	m	m	m	m	m	
CV758	Position 2 ls	l	l	l	l	l	l	l	l	
CV759	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	
CV760	Option type 2 / speed 2	t	t	t	s	s	s	s	s	
CV761	Position 3 ms	m	m	m	m	m	m	m	m	
CV762	Position 3 ls	l	l	l	l	l	l	l	l	
CV763	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	
CV764	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV765	Position 4 ms	m	m	m	m	m	m	m	m	
CV766	Position 4 ls	l	l	l	l	l	l	l	l	
CV767	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV768	Option type 4 / speed 4	t	t	t	s	s	s	s	s	
<b>Stepper Table entry #9</b>										
CV769	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type 000 = normal 001 = rebound 010 = continuous left 011 = continuous right 100 = repeat left 101 = repeat right 110 = xx 111 = stop (sets position = 0)
CV770	Position 1 ls	l	l	l	l	l	l	l	l	
CV771	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV772	Option type 1 / speed 1	t	t	t	s	s	s	s	s	
CV773	Position 2 ms	m	m	m	m	m	m	m	m	
CV774	Position 2 ls	l	l	l	l	l	l	l	l	
CV775	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	
CV776	Option type 2 / speed 2	t	t	t	s	s	s	s	s	





CV843	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	
CV844	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV845	Position 4 ms	m	m	m	m	m	m	m	m	
CV846	Position 4 ls	l	l	l	l	l	l	l	l	
CV847	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV848	Option type 4 / speed 4	t	t	t	s	s	s	s	s	
Stepper Table entry #14										
CV849	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type 000 = normal 001 = rebound 010 = continuous left 011 = continuous right 100 = repeat left 101 = repeat right 110 = xx 111 = stop (sets position = 0)  sssss = Speed
CV850	Position 1 ls	l	l	l	l	l	l	l	l	
CV851	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV852	Option type 1 / speed 1	t	t	t	s	s	s	s	s	
CV853	Position 2 ms	m	m	m	m	m	m	m	m	
CV854	Position 2 ls	l	l	l	l	l	l	l	l	
CV855	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	
CV856	Option type 2 / speed 2	t	t	t	s	s	s	s	s	
CV857	Position 3 ms	m	m	m	m	m	m	m	m	
CV858	Position 3 ls	l	l	l	l	l	l	l	l	
CV859	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	
CV860	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV861	Position 4 ms	m	m	m	m	m	m	m	m	
CV862	Position 4 ls	l	l	l	l	l	l	l	l	
CV863	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV864	Option type 4 / speed 4	t	t	t	s	s	s	s	s	
Stepper Table entry #15										
CV865	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type 000 = normal 001 = rebound 010 = continuous left 011 = continuous right 100 = repeat left 101 = repeat right 110 = xx 111 = stop (sets position = 0)  sssss = Speed
CV866	Position 1 ls	l	l	l	l	l	l	l	l	
CV867	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV868	Option type 1 / speed 1	t	t	t	s	s	s	s	s	
CV869	Position 2 ms	m	m	m	m	m	m	m	m	
CV870	Position 2 ls	l	l	l	l	l	l	l	l	
CV871	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	
CV872	Option type 2 / speed 2	t	t	t	s	s	s	s	s	
CV873	Position 3 ms	m	m	m	m	m	m	m	m	
CV874	Position 3 ls	l	l	l	l	l	l	l	l	
CV875	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	

CV876	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV877	Position 4 ms	m	m	m	m	m	m	m	m	
CV878	Position 4 ls	l	l	l	l	l	l	l	l	
CV879	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV880	Option type 4 / speed 4	t	t	t	s	s	s	s	s	
Stepper Table entry #16										
CV881	Position 1 ms	m	m	m	m	m	m	m	m	mmmmmmmm=Position hi bits 1111111l=Position lo bits  aaaa = Acceleration Rate dddd = Deceleration Rate  ttt = Option Type 000 = normal 001 = rebound 010 = continuous left 011 = continuous right 100 = repeat left 101 = repeat right 110 = xx 111 = stop (sets position = 0)  sssss = Speed
CV882	Position 1 ls	l	l	l	l	l	l	l	l	
CV883	Acceleration/Deceleration 1	a	a	a	a	d	d	d	d	
CV884	Option type 1 / speed 1	t	t	t	s	s	s	s	s	
CV885	Position 2 ms	m	m	m	m	m	m	m	m	
CV886	Position 2 ls	l	l	l	l	l	l	l	l	
CV887	Acceleration/Deceleration 2	a	a	a	a	d	d	d	d	
CV888	Option type 2 / speed 2	t	t	t	s	s	s	s	s	
CV889	Position 3 ms	m	m	m	m	m	m	m	m	
CV890	Position 3 ls	l	l	l	l	l	l	l	l	
CV891	Acceleration/Deceleration 3	a	a	a	a	d	d	d	d	
CV892	Option type 3 / speed 3	t	t	t	s	s	s	s	s	
CV893	Position 4 ms	m	m	m	m	m	m	m	m	
CV894	Position 4 ls	l	l	l	l	l	l	l	l	
CV895	Acceleration/Deceleration 4	a	a	a	a	d	d	d	d	
CV896	Option type 4 / speed 4	t	t	t	s	s	s	s	s	

897-1024	NMRA Reserved = 128 bytes

Byte totals:

Range	Used	Comment
1-32	7	NMRA Info area
33-64		
112-112	1	Configuration options
121-128		
128-639	512	Input/Output Control
640-895	256	Stepper Tables

Total